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APPLICANT

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****CONTINUING DOMESTIC DATA*******

VERIFIED THIS APPLN IS A CON OF PCT/DE97/03013 12/21/97
AND A CIP OF 08/947,254 10/08/97, U.S. Patent No. 6,119,181

yes GCR
11-30-2000

****371 (NAT'L STAGE) DATA*******

VERIFIED

none GCR
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****FOREIGN APPLICATIONS*******

VERIFIED FED REP GERMANY 196 54 595.1 12/20/96

yes GCR
11-30-2000

IF REQUIRED, FOREIGN FILING LICENSE GRANTED 07/14/99 ** SMALL ENTITY **

Foreign Priority claimed 35 USC 119 (a-d) conditions met. <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY DEX	SHEETS DRAWING 18	TOTAL CLAIMS 18	INDEPENDENT CLAIMS 4
Verified and Acknowledged <i>GCR 11/30/00</i> Examiner's Initials _____					

ADDRESS

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TITLE

I/O AND MEMORY BUS SYSTEM FOR DFPS AND UNITS WITH TWO OR MULTI-DIMENSIONAL PROGRAMMABLE CELL ARCHITECTURES

FILING FEE RECEIVED \$445	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT NO. _____ for the following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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